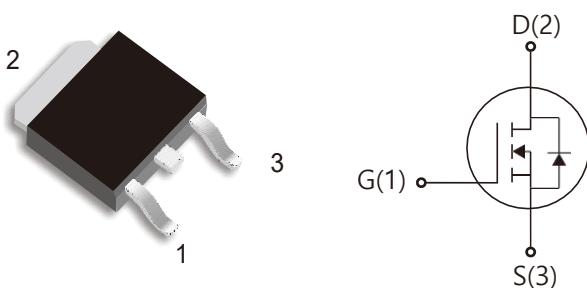
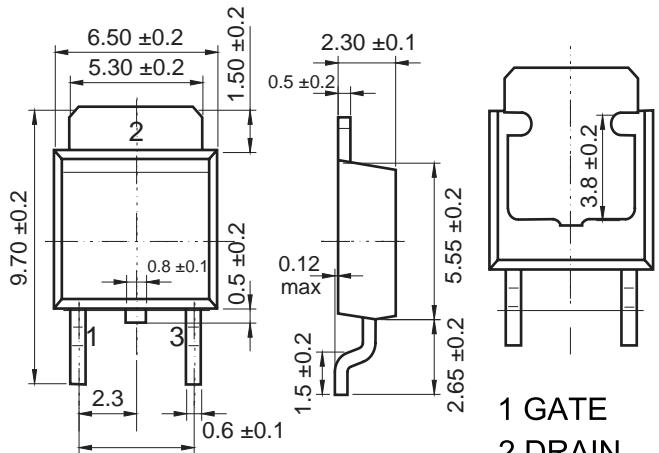


**Features:**

- Low Intrinsic Capacitances.
- Excellent Switching Characteristics.
- Extended Safe Operating Area.
- Unrivalled Gate Charge : $Q_g = 20\text{nC}$  (Typ.).
- $BVDSS=400\text{V}$ ,  $I_D=6\text{A}$
- $R_{DS(on)} : 1.0\Omega$  (Max) @  $V_G=10\text{V}$
- 100% Avalanche Tested


**TO-252**

**Absolute Maximum Ratings\* (Tc=25°C Unless otherwise noted)**

Symbol	PARAMETER	Value	Unit
$V_{DSS}$	Drain-Source Voltage	400	V
$I_D$	Drain Current	$T_c=25^\circ\text{C}$	6
		$T_c=100^\circ\text{C}$	3.6
$V_{GS(TH)}$	Gate Threshold Voltage	$\pm 30$	V
$E_{AS}$	Single Pulse Avalanche Energy (note1)	270	mJ
$I_{AR}$	Avalanche Current (note2)	6	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	74	W
$T_j$	Junction Temperature(MAX)	150	°C
$T_{stg}$	Storage Temperature	-55~+150	°C
$TL$	Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds	300	°C

**Thermal Characteristics**

Symbol	PARAMETER	Typ.	MAX.	Unit
$R_{\theta JC}$	Thermal Resistance,Junction to Case	-	1.68	°C/W
$R_{\theta JA}$	Thermal Resistance,Junction to Ambient	-	100	°C/W

**Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Test Condition	MIN.	Typ.	MAX.	Unit
<b>Off Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> =250μA, V <sub>GS</sub> =0	400	-	-	V
△BV <sub>DSS</sub> /△T <sub>J</sub>	Breakdown Voltage Temperature Confident	I <sub>D</sub> =250μA, Reference to 25°C	-	0.54	-	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =400V, V <sub>GS</sub> =0V	-	-	1	μA
		V <sub>DS</sub> =320V, T <sub>c</sub> =125°C			10	
I <sub>GSSF</sub>	Gate-body leakage Current, Forward	V <sub>GS</sub> =+30V, V <sub>DS</sub> =0V	-	-	100	nA
I <sub>GSSR</sub>	Gate-body leakage Current, Reverse	V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V	-	-	-100	nA
<b>On Characteristics</b>						
V <sub>GS(TH)</sub>	Date Threshold Voltage	I <sub>D</sub> =250μA, V <sub>DS</sub> =V <sub>GS</sub>	2	-	4	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	I <sub>D</sub> =3A, V <sub>GS</sub> =10V	-	0.83	1.0	Ω
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0, f=1.0MHz	-	880	-	pF
C <sub>oss</sub>	Output Capacitance		-	80	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	15	-	pF
<b>Switching Characteristics</b>						
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =200V, I <sub>D</sub> =10A R <sub>G</sub> =25Ω (Note 3,4)	-	-	35	ns
T <sub>r</sub>	Turn-On Rise Time		-	-	140	ns
T <sub>d(off)</sub>	Turn-Off Delay Time		-	-	55	ns
T <sub>f</sub>	Turn-Off Rise Time		-	-	85	ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =320V, V <sub>GS</sub> =10V, I <sub>D</sub> =6A (Note 3,4)	-	20	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	2.3	-	nC
Q <sub>gd</sub>	Gate-Drain Charge		-	6.4	-	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>s</sub>	Max. Diode Forward Current	-	-	-	6	A
I <sub>SM</sub>	Max. Pulsed Forward Current	-	-	-	24	A
V <sub>SD</sub>	Diode Forward Voltage	I <sub>D</sub> =6A	-	-	1.5	V
T <sub>rr</sub>	Reverse Recovery Time	I <sub>s</sub> =6A, V <sub>GS</sub> =0V dI/dt=100A/μs (Note 3)	-	320	-	nS
Q <sub>rr</sub>	Reverse Recovery Charge		-	2.4	-	μC

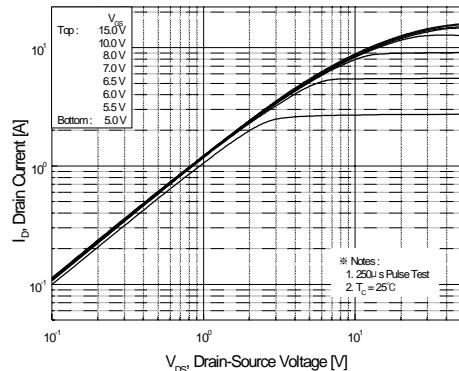
Notes : 1, L=17.9mH, IAS=5.5A, VDD=50V, RG=25Ω, Starting TJ =25°C

2, Repetitive Rating : Pulse width limited by maximum junction temperature

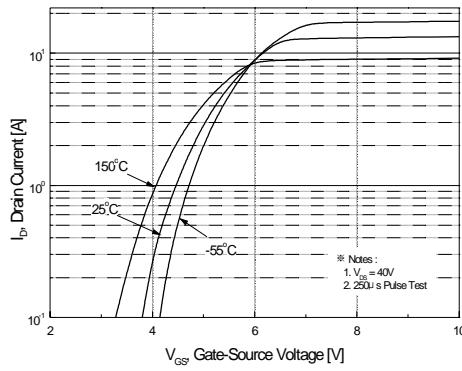
3, Pulse Test : Pulse Width ≤ 300μs, Duty Cycle ≤ 2%

4, Essentially Independent of Operating Temperature

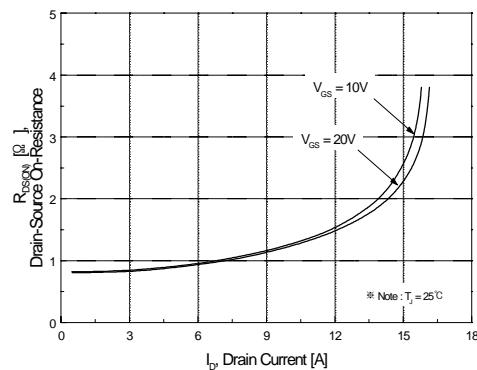
## Typical Characteristics



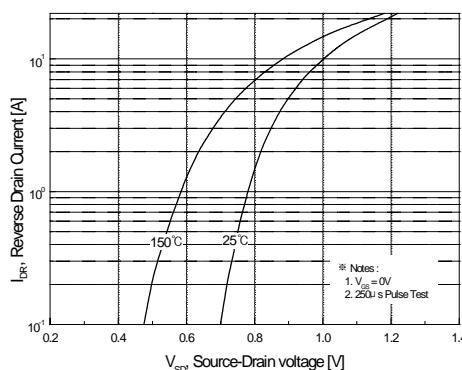
**Figure 1. On-Region Characteristics**



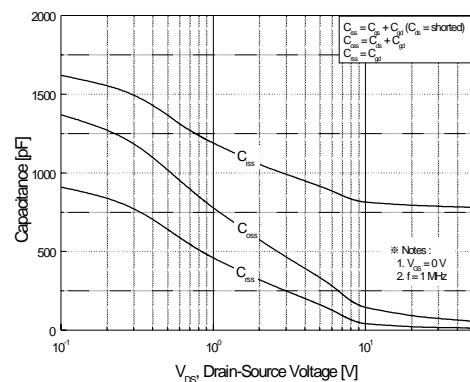
**Figure 2. Transfer Characteristics**



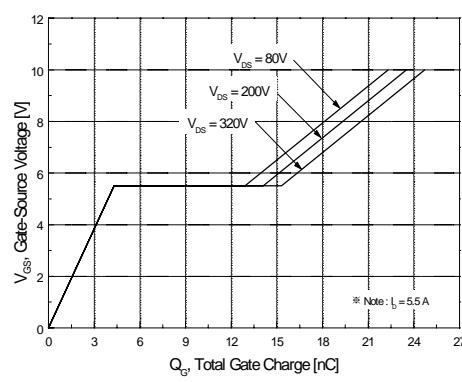
**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



**Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature**

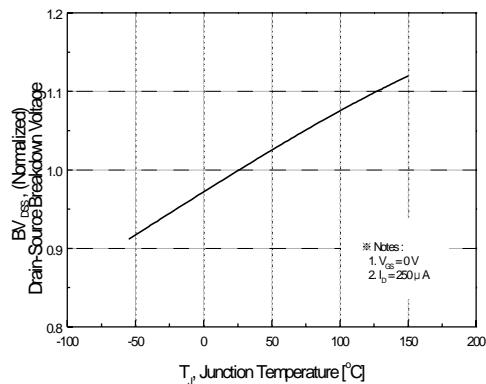


**Figure 5. Capacitance Characteristics**

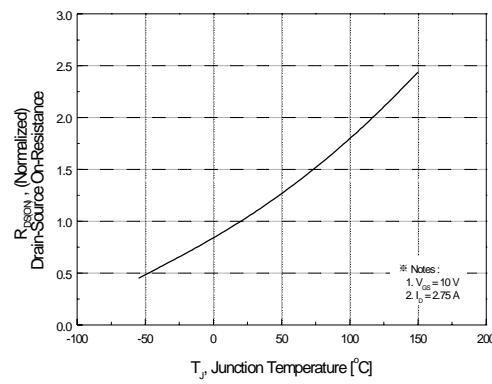


**Figure 6. Gate Charge Characteristics**

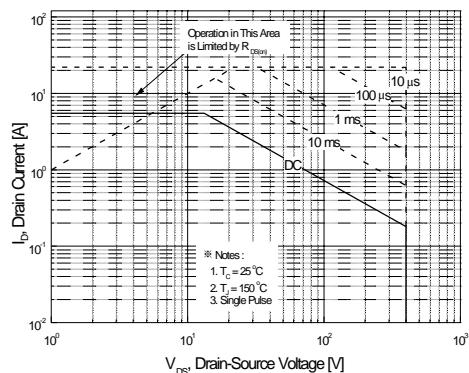
## Typical Characteristics (Continued)



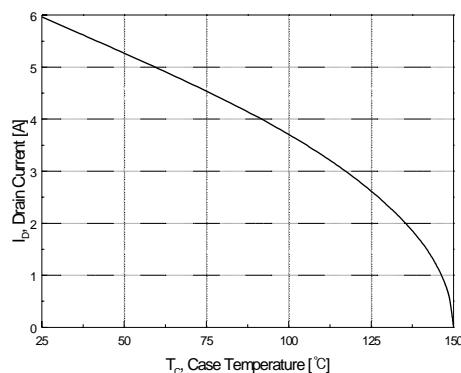
**Figure 7. Breakdown Voltage Variation  
vs Temperature**



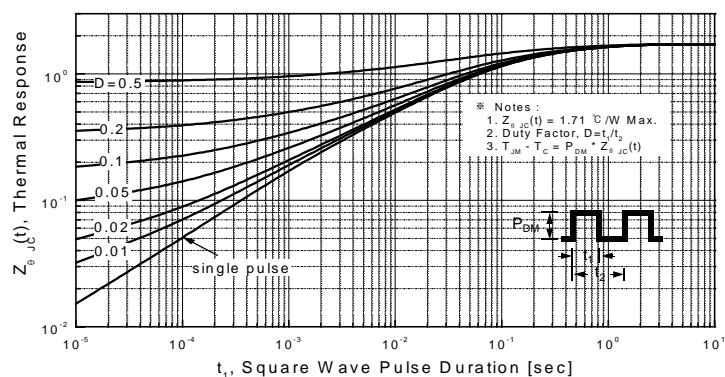
**Figure 8. On-Resistance Variation  
vs Temperature**



**Figure 9. Maximum Safe Operating Area**

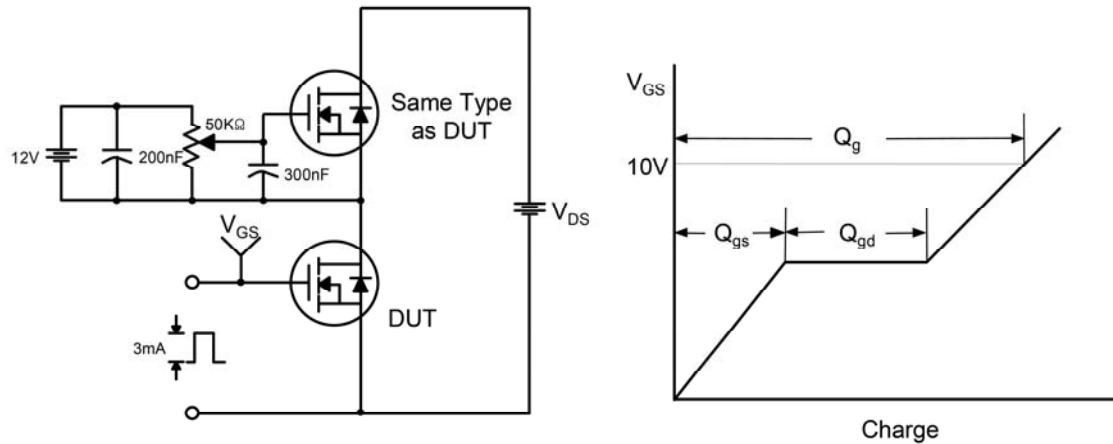


**Figure 10. Maximum Drain Current  
vs Case Temperature**

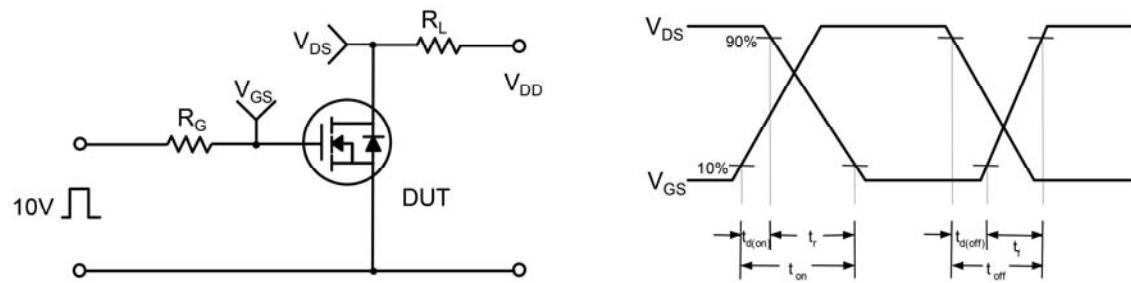


**Figure 11. Transient Thermal Response Curve**

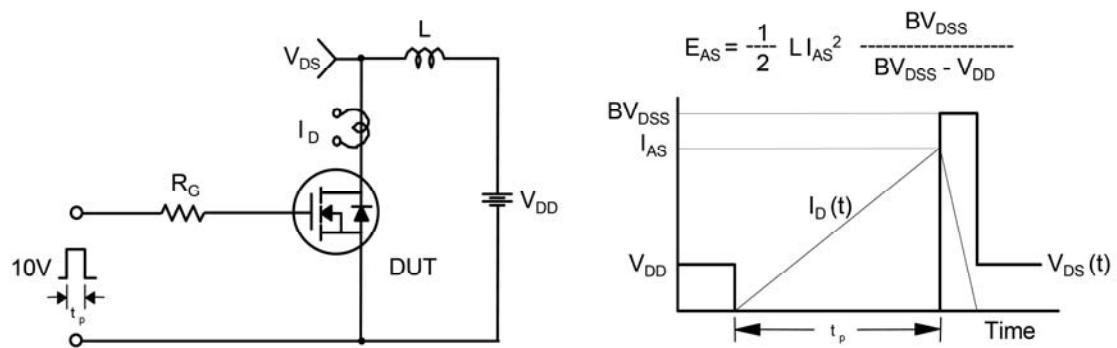
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms



### Unclamped Inductive Switching Test Circuit & Waveforms



**Peak Diode Recovery dv/dt Test Circuit & Waveform**
