



Features

- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$

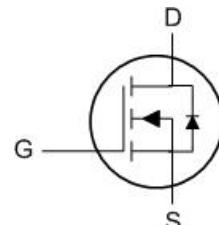
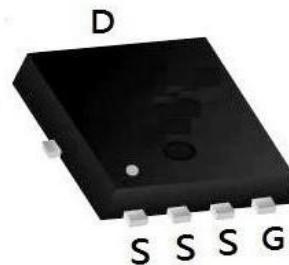
Product Summary

BVDSS	RDSON	ID
30V	3.8mΩ	65A

PRPAK5X6 Pin Configuration

Applications

- DC-DC Converters
- Power management functions
- Synchronous-rectification applications



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current T _C =25°C	I_D	65	A
T _C =100°C		41	
Pulsed Drain Current ¹	I_{DM}	260	A
Single Pulse Avalanche Energy ²	E_{AS}	20	mJ
Total Power Dissipation	P_D	31.25	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	$R_{\theta JA}$	60	°C/W
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	4	°C/W

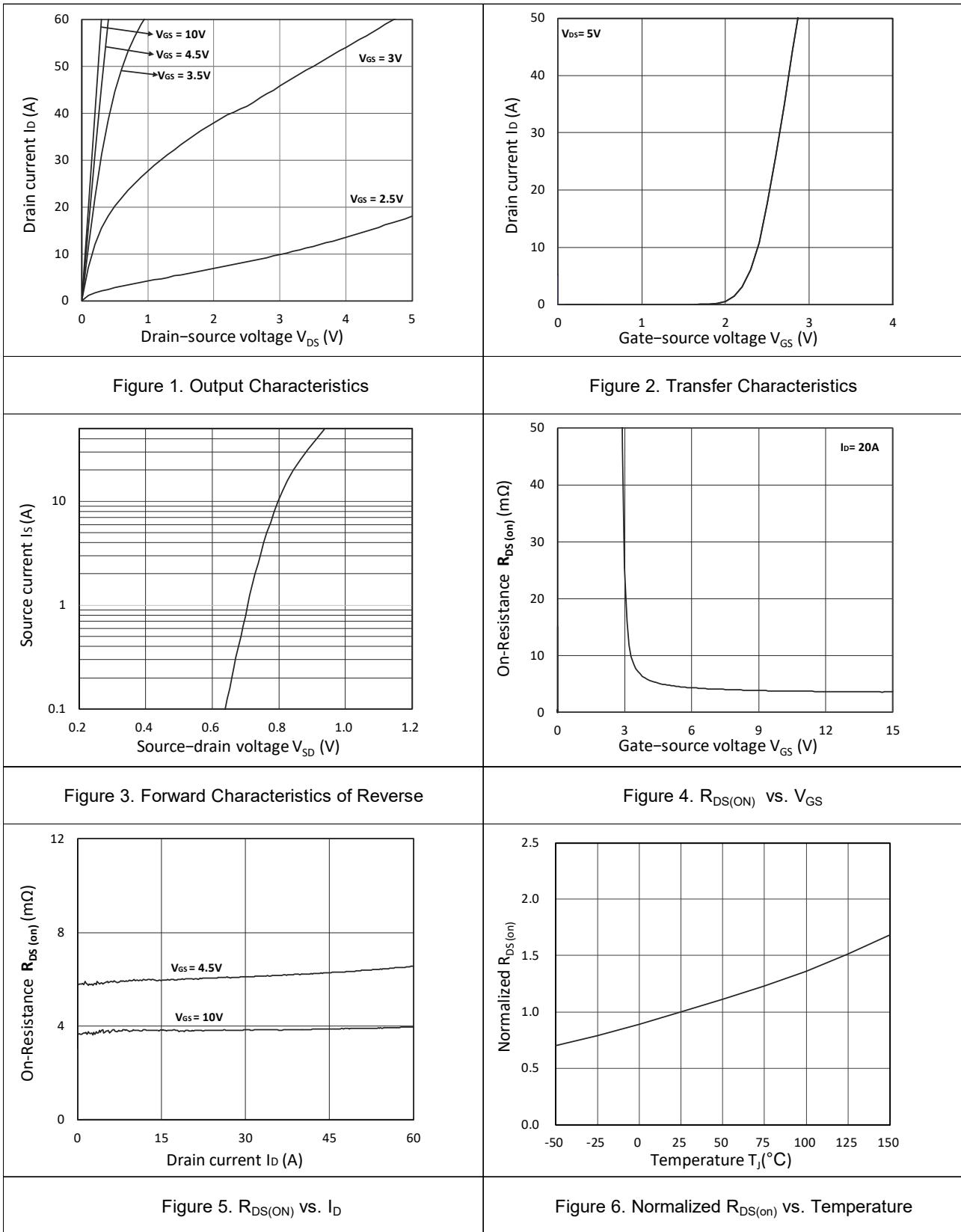
Electrical Characteristics ($T_J = 25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30	-	-	V
Gate-body Leakage Current	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current $T_J=25^\circ\text{C}$	I_{DSS}	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}$	-	-	1	μA
$T_J=100^\circ\text{C}$			-	-	100	
Gate-Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.2	1.6	2.4	V
Drain-Source On-Resistance ⁴	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$	-	3.8	5	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 10\text{A}$	-	6	8	
Forward Transconductance ⁴	g_{fs}	$V_{\text{DS}} = 10\text{V}, I_D = 20\text{A}$	-	130	-	S
Dynamic Characteristics⁵						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 15\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$	-	905	-	pF
Output Capacitance	C_{oss}		-	475	-	
Reverse Transfer Capacitance	C_{rss}		-	57	-	
Gate Resistance	R_g	$f = 1\text{MHz}$	-	1.9	-	Ω
Switching Characteristics⁵						
Total Gate Charge	Q_g	$V_{\text{GS}} = 10\text{V}, V_{\text{DS}} = 15\text{V}, I_D = 20\text{A}$	-	16	-	nC
Gate-Source Charge	Q_{gs}		-	3	-	
Gate-Drain Charge	Q_{gd}		-	3.3	-	
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, V_{\text{DD}} = 15\text{V}, R_G = 3\Omega, I_D = 20\text{A}$	-	6.3	-	ns
Rise Time	t_r		-	3.2	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	18	-	
Fall Time	t_f		-	3.6	-	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	10	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	13.2	-	nC
Drain-Source Body Diode Characteristics						
Diode Forward Voltage ⁴	V_{SD}	$I_S = 20\text{A}, V_{\text{GS}} = 0\text{V}$	-	-	1.2	V
Continuous Source Current	$T_C = 25^\circ\text{C}$	I_S	-	-	65	A

Notes:

- Repetitive rating, pulse width limited by junction temperature $T_{J(\text{MAX})} = 150^\circ\text{C}$.
- The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=25\text{V}, V_{\text{GS}}=10\text{V}, L=0.1\text{mH}, I_{\text{AS}}=20\text{A}$.
- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics



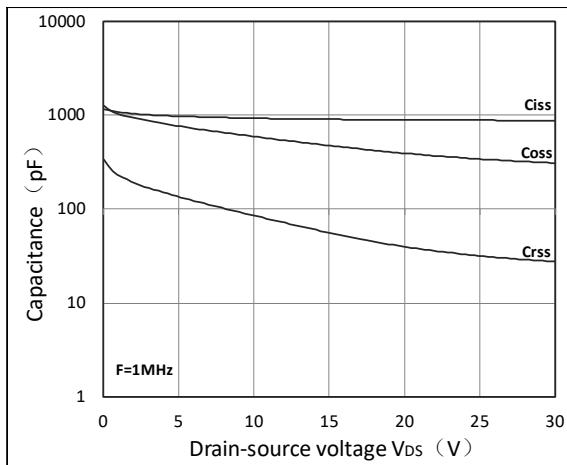


Figure 7. Capacitance Characteristics

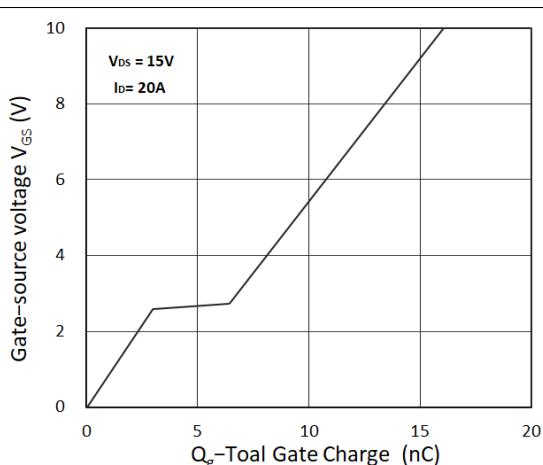


Figure 8. Gate Charge Characteristics

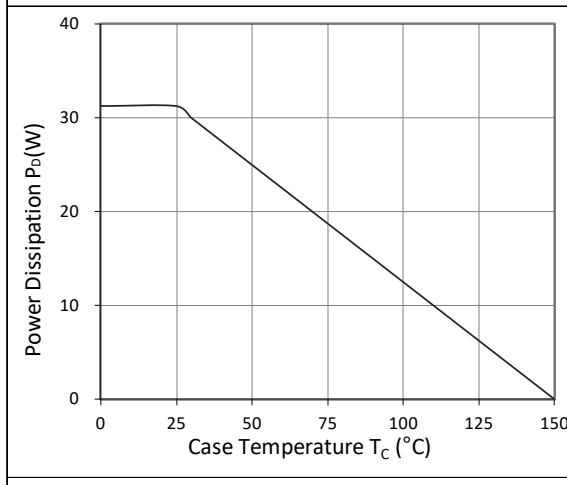


Figure 9. Power Dissipation

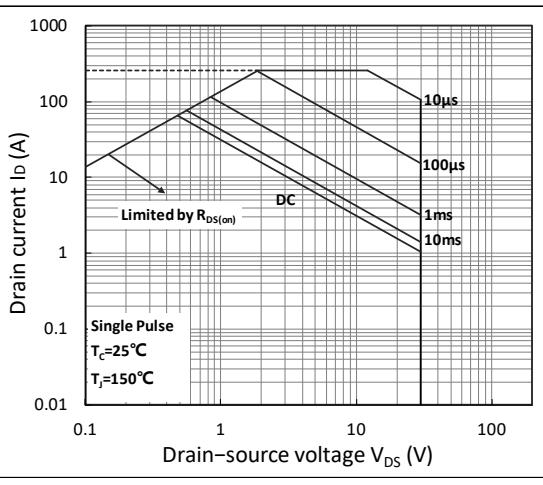


Figure 10. Safe Operating Area

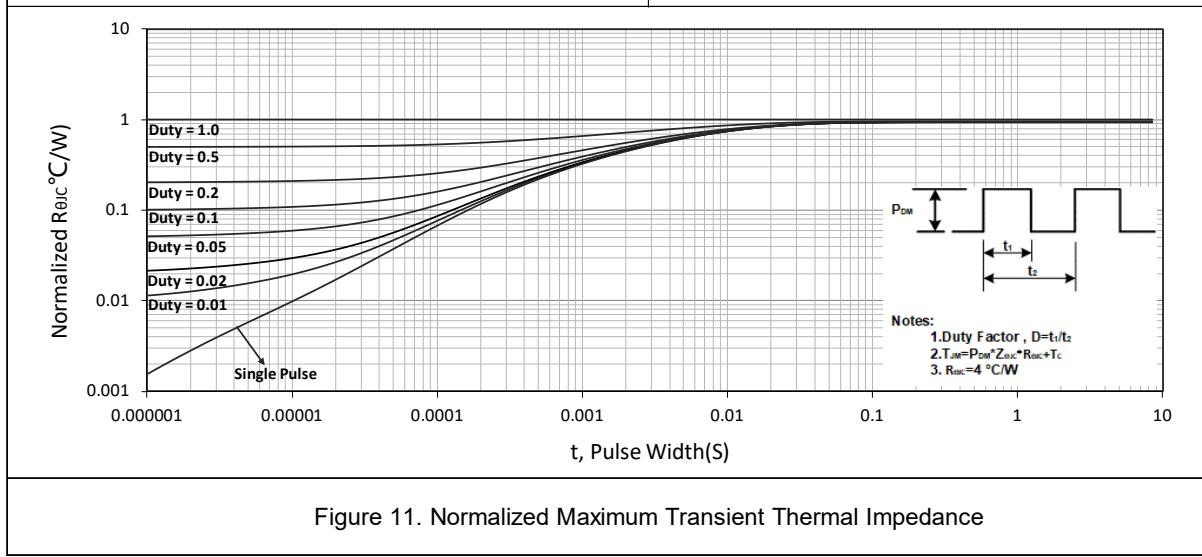
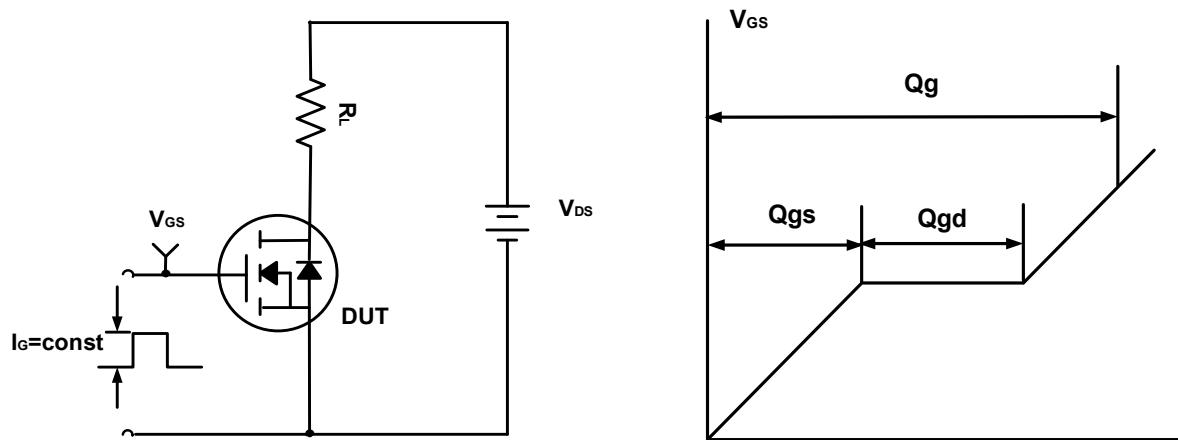
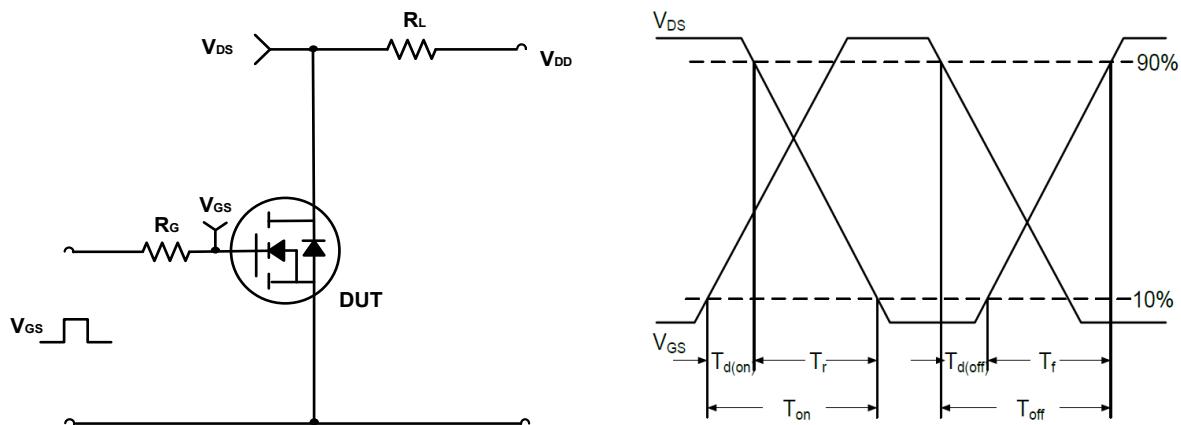
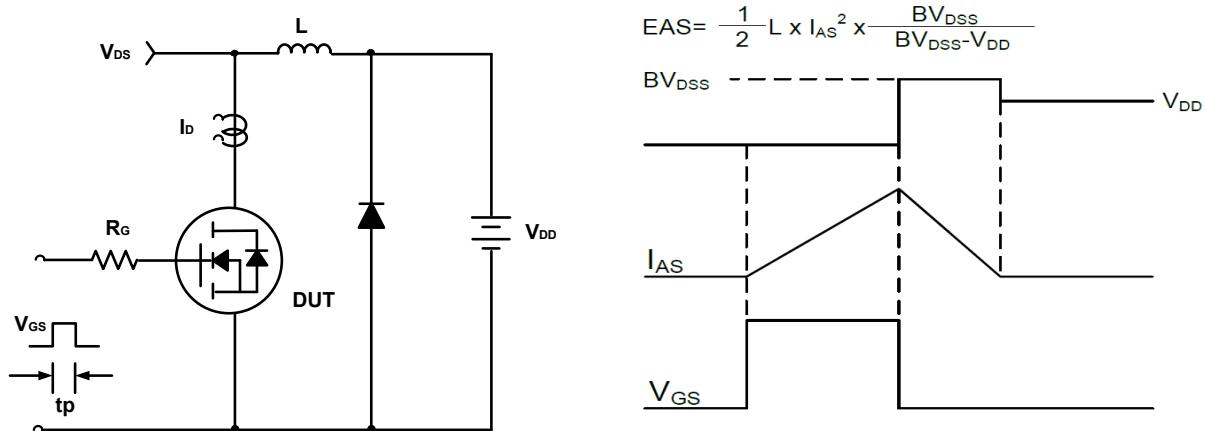
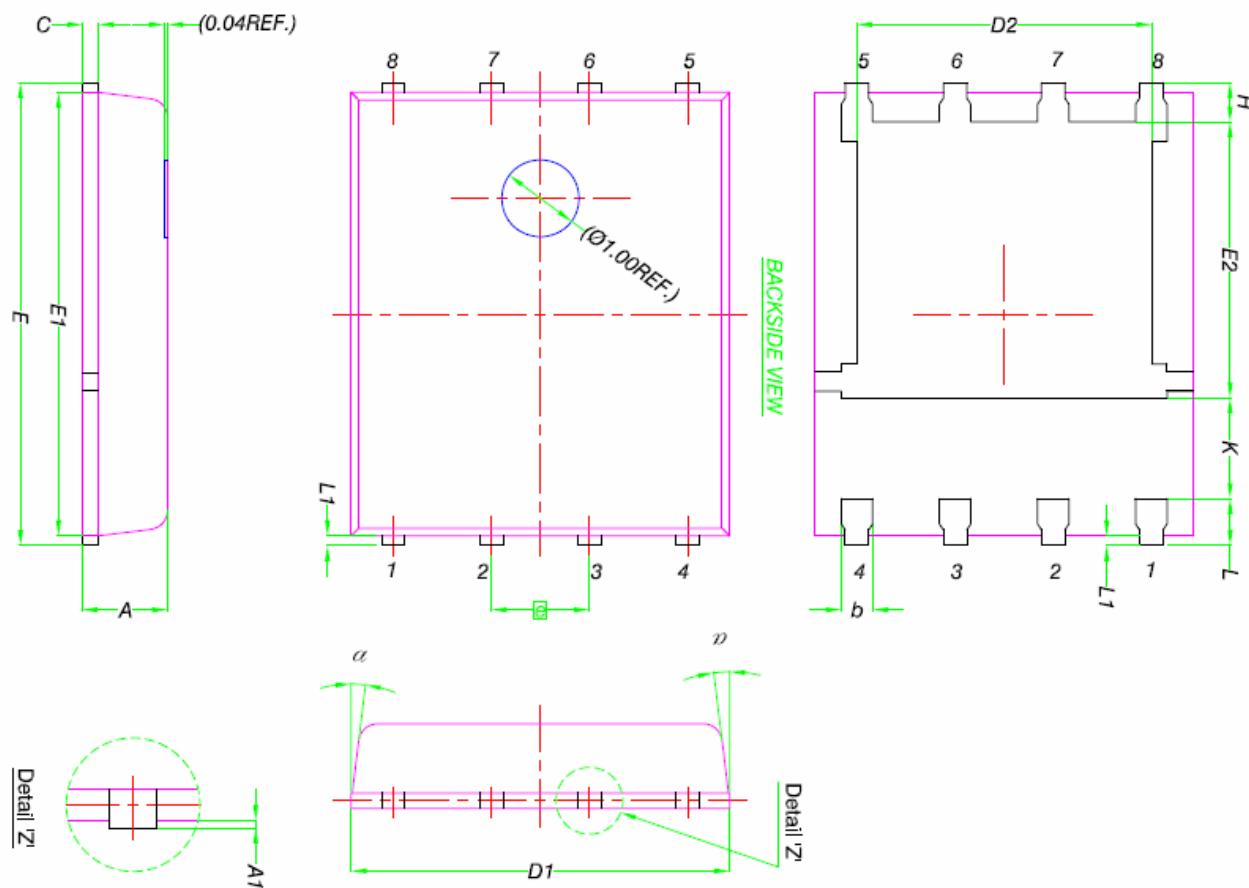


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

DFN5X6-8L Package Information


Figure A. Gate Charge Test Circuit & Waveforms

Figure B. Switching Test Circuit & Waveforms

Figure C. Unclamped Inductive Switching Circuit & Waveforms

DFN5X6-8L Package Information


DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
A1	0	-	0.05
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
[e] 1.27 BSC			
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
α	0°	-	12°

